

Claims

- [1] An encryption processor, comprising:
an encryption processor which connects an externally connected data input and output apparatus and an internal data process apparatus and mediates a communication between the same;
an password process unit which encrypts an externally inputted data based on a certain encryption algorithm; and
a memory unit which stores a program corresponding to the encryption algorithm and temporarily stores a data generated during an encryption process, wherein the above encryption processor, password process unit and memory unit are integrated into one independent chip.
- [2] An encryption processor, comprising:
a video process module which includes a second interface for managing a connection of an externally connected input and output apparatus, a coder for compressing the externally inputted data into a certain format, and a decoder for decompressing the compressed video data; and
an encryption module which includes a first interface for managing a connection of an externally connected input and output apparatus, and a password process unit for encrypting the video data using a certain encryption algorithm and decoding the encrypted video data using a certain decoding algorithm corresponding to the encryption algorithm, wherein the above video process module and encryption module are integrated into one independent chip.
- [3] An encryption processor, comprising:
a video process module which includes a second interface for managing a connection of an externally connected input and output apparatus, a video adjusting unit for adjusting a recording environment including a focus, exposure and lighting of an externally received video data, a coder for compressing the video data into a certain format, and a decoder for decompressing the compressed video data; and
an encryption module which includes a first interface for managing a connection of an externally connected input and output apparatus, and a password process unit for encrypting the video data using a certain encryption algorithm and decoding the encrypted video data using a certain decoding algorithm corresponding to the encryption algorithm, wherein the above video process module and encryption module are integrated into one independent chip.
- [4] An encryption processor, comprising:
a video process module which includes a second interface for managing a

connection of an externally connected input and output apparatus, a video data generation unit for converting an externally inputted electric signal into a video data, a video adjusting unit for adjusting a recording environment including a focus, exposure and lighting of the video data, a coder for compressing the video data into a certain format, and a decoder for decompressing the compressed data; an encryption module which includes a first interface for managing a connection of an externally connected input and output apparatus, and a password process unit for encrypting the video data using a certain encryption algorithm and decoding the encrypted video data using a certain decoding algorithm corresponding to the encryption algorithm, wherein the above video process module and encryption module are integrated into one independent chip.

[5]

An encryption processor, comprising:

a video process module which includes a second interface for managing a connection of an externally connected input and output apparatus, a signal compensation unit for removing noises from an externally inputted electric signal and compensating the signal, a video data generation unit for converting the electric signal into a video data, a video adjusting unit for adjusting a recording environment including a focus, exposure and lighting of the video data, a coder for compressing the video data into a certain format, and a decoder for decompressing the compressed video data; and

an encryption module which includes a first interface for managing a connection of an externally connected input and output apparatus, and a password process unit for encrypting the video data using a certain encryption algorithm and decoding the encrypted video data using a certain decoding algorithm corresponding to the encryption algorithm, wherein the above video process module and encryption module are integrated into one independent chip.

[6]

An encryption processor, comprising:

a video process module which includes a second interface for managing a connection of an externally connected input and output apparatus, a charge coupled device (CCD) for converting an externally inputted light signal into an electric signal, a signal compensation unit for removing noises from the electric signal from the CCD and compensating the signal, a video data generation unit for converting the electric signal into a video data, a video adjusting unit for adjusting a recording environment including a focus, exposure and lighting of the video data by controlling the CCD or the signal compensation unit, a coder for compressing the video data into a certain format, and a decoder for decompressing the compressed video data; and

an encryption module which includes a first interface for managing a connection

of an externally connected input and output apparatus, and a password process unit for encrypting the video data using a certain encryption algorithm and decoding the encrypted video data using a certain decoding algorithm corresponding to the encryption algorithm, wherein the above video process module and encryption module are integrated into one independent chip.

- [7] The apparatus of one among claims 1 through claim 6, further comprising an encryption controller for externally receiving a signal with respect to an operation state of the password process unit and controlling an operation of the password process unit, wherein the encryption controller is adapted to control a size of a password used for an encryption or an encryption operation mode.
- [8] The apparatus of one among claim 1 through claim 6, further comprising a communication module for transferring an internally converted data or a generated data through an internally connected communication network.
- [9] The apparatus of one among claim 1 through claim 6, wherein password process unit detects an externally received abnormal signal and deletes a certain data for a data encryption corresponding to the abnormal signal receipt and an encrypted video data.
- [10] The apparatus of one among claim 2 through 6, wherein an externally inputted password is directly inputted into the encryption module for a data encryption.
- [11] The apparatus of one among claim 2 through claim 6, wherein an externally or internally generated video data is transferred to the password process unit through the second interface and the first interface.
- [12] The apparatus of one among claim 2 through claim 6, wherein said password process unit generates a password used for a data encryption through the first interface in communication with the password input apparatus.